

Fig. 1

FIG. 2 is a block diagram of a system 200, including a first device 150 and a second device 100a, connected via a communication link 170-174. The first device 150 includes a processor 152 and a communication device. The second device 100a includes a communication device, a codec subsystem, and various input/output components (102a, 102b, 104, 106, 120, 122, 124).

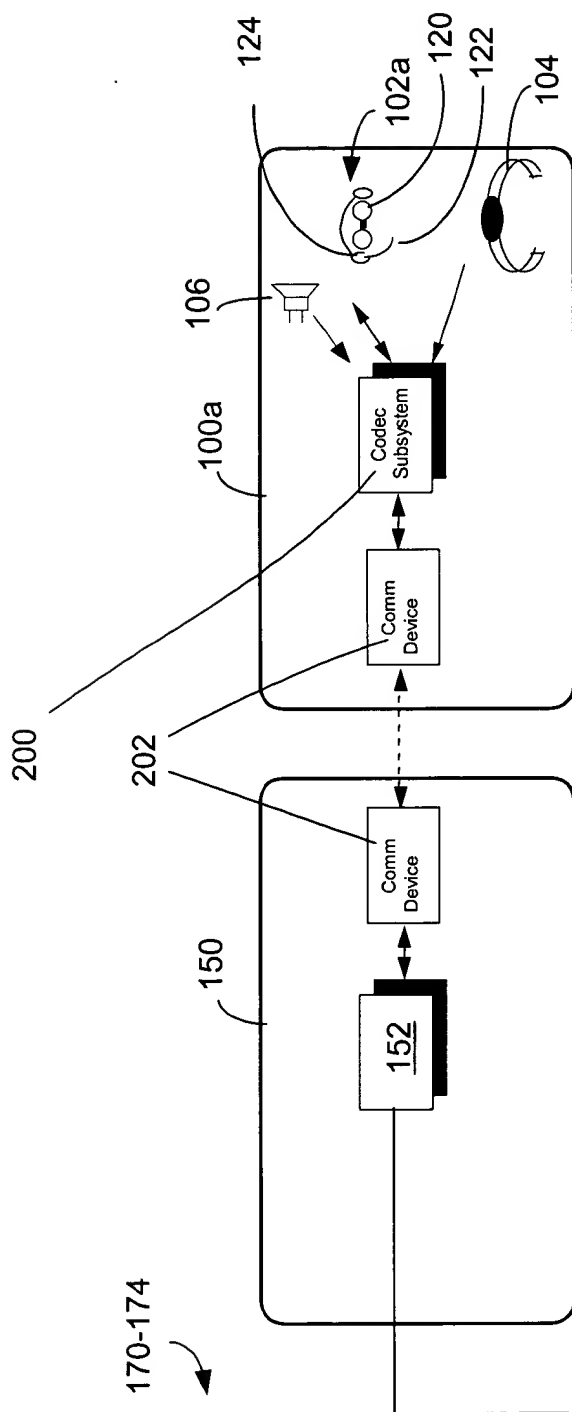


Fig. 2

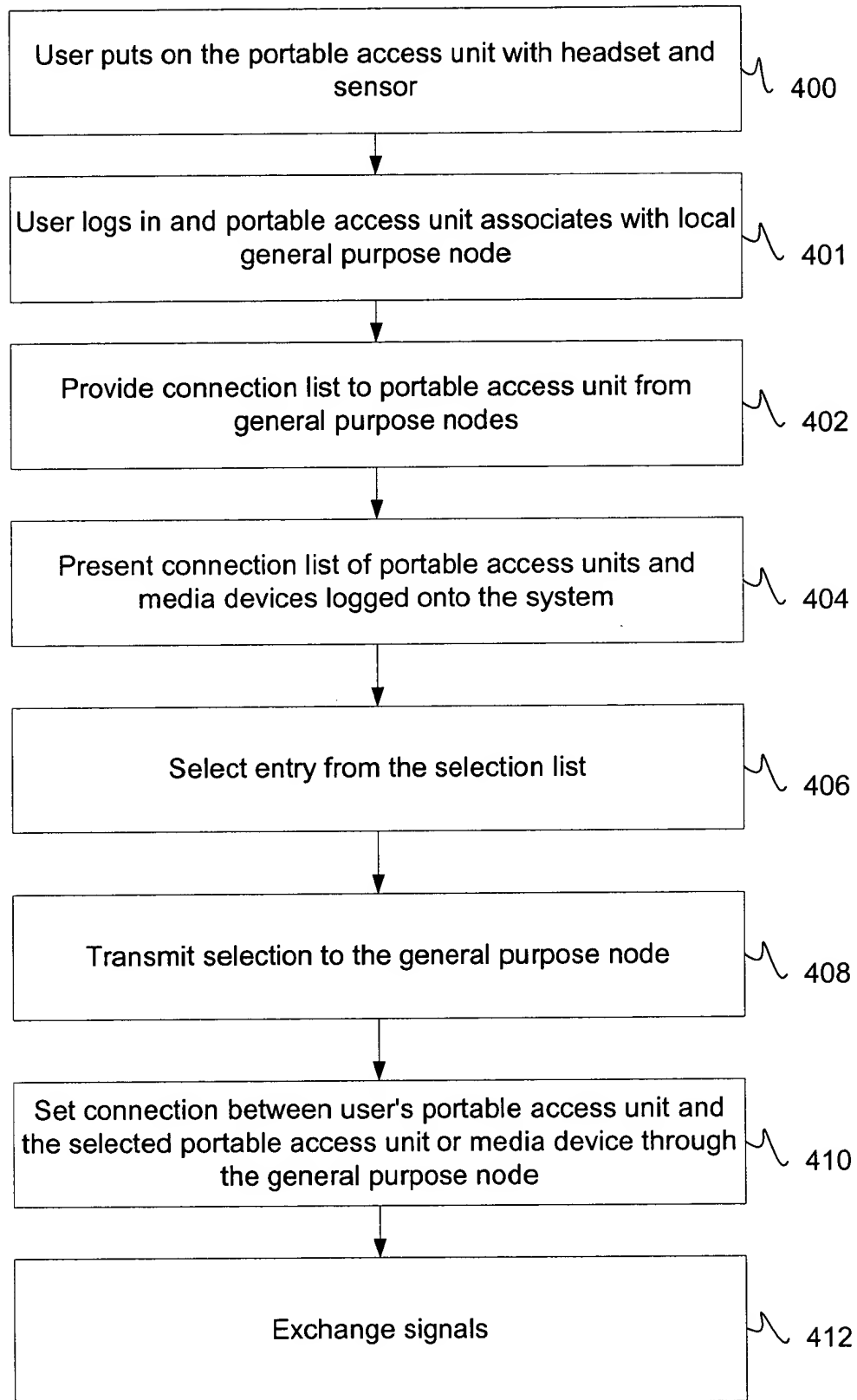


Fig. 3